



# VLSI Synthesis of DSP Kernels: Algorithmic and Architectural Transformations

*Mahesh Mehendale, Sunil D. Sherlekar*

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A critical step in the design of a DSP system is to identify for each of its components an implementation architecture that provides the desired degree of flexibility/programmability and optimises the area-delay-power parameters. This essential book covers architectures that offer varying degrees of programmability.

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